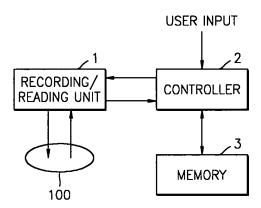
FIG. 1



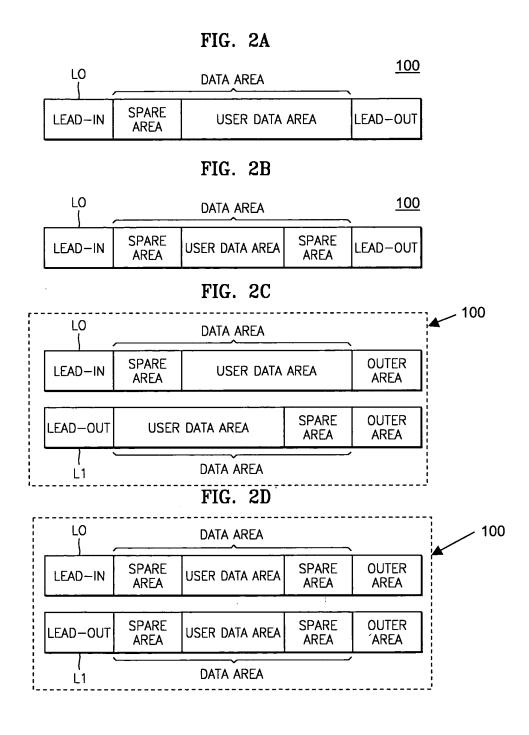


FIG. 3A

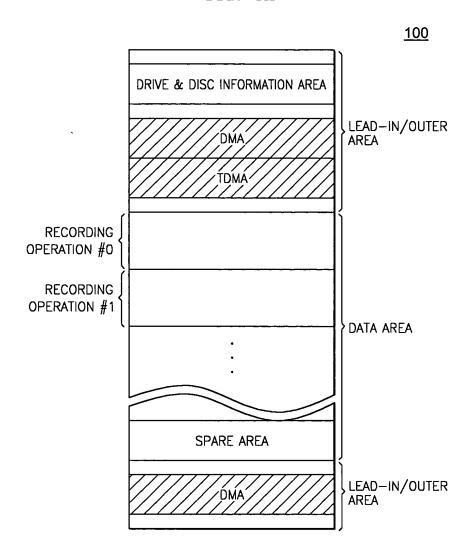


FIG. 3B

DMA 2				
BUFFER 3				
TEST				
TDMA				
DMA 1				
BUFFER 2				
DRIVE AND DISC INFORMATION				
BUFFER 1				

FIG. 4A

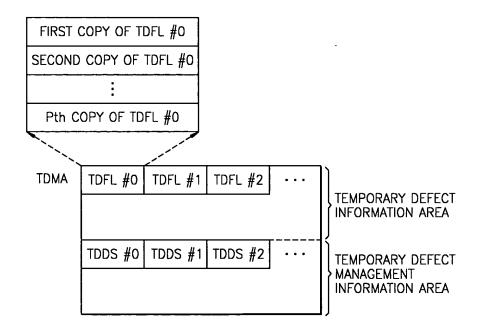


FIG. 4B

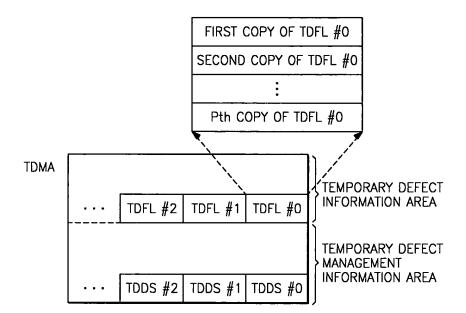


FIG. 4C

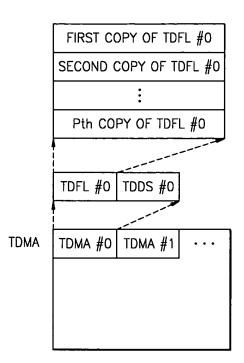


FIG. 4D

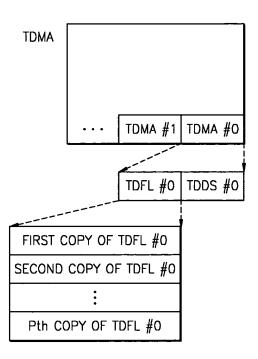


FIG. 5A

TDMA	TDDS #0	TDDS #1	• • •

FIG. 5B

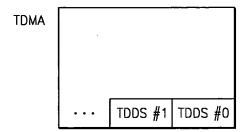


FIG. 6A

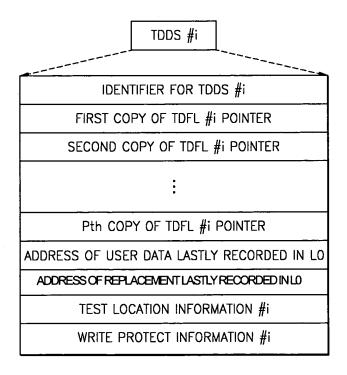


FIG. 6B

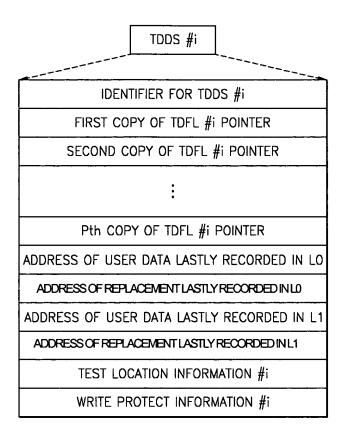


FIG. 7

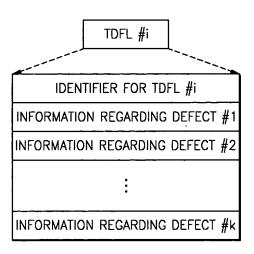
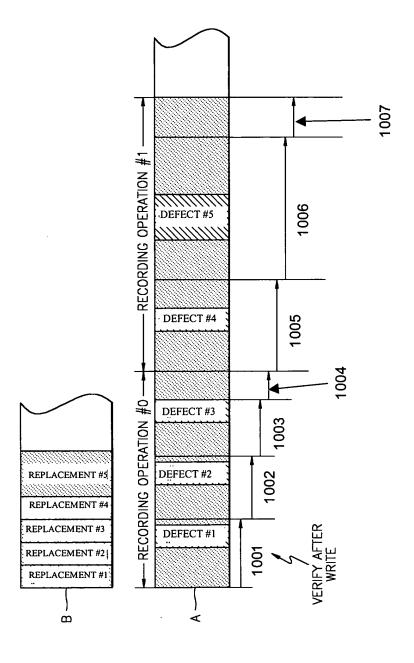


FIG. 8A

DRIVE & DISC INFORMATION AREA	WRITE PROTECT INFORMATION #0	TDFL #0 Pointer	TDDS #0 POINTER	• • •
ANLA				

FIG. 8B

DRIVE & DISC INFORMATION AREA	WRITE PROTECT INFORMATION #0	TDMA #0 POINTER	





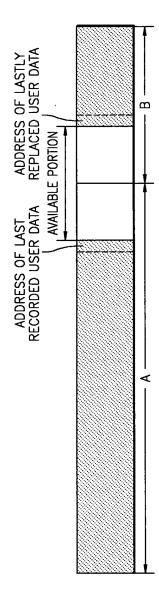


FIG. 11A

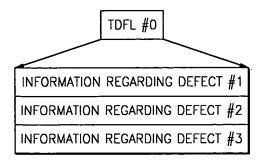


FIG. 11B

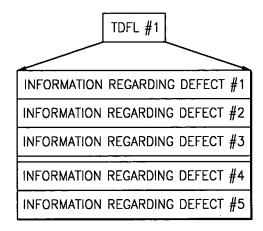


FIG. 12

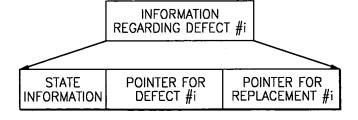


FIG. 13A

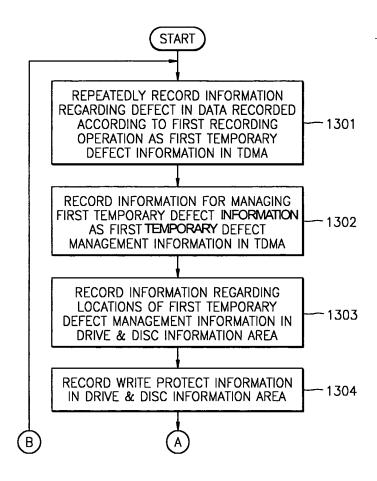


FIG. 13B

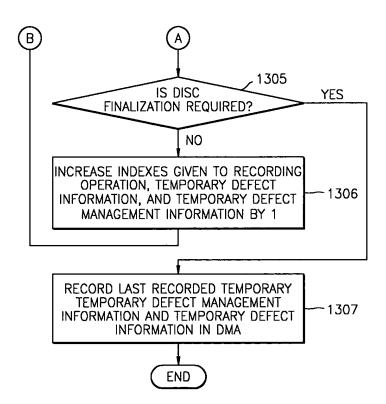


FIG. 14A

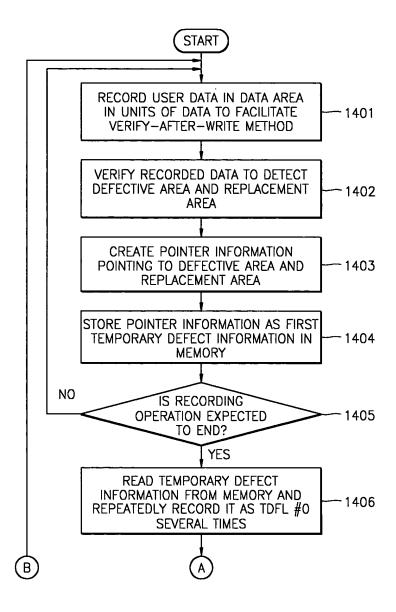


FIG. 14B

